



*Datcenter Performance. Edge Efficiency.  
Accelerating Inference, Everywhere.*



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## **Introduction of Axelera AI**

**AI Inference Made Simple**

**ESA CommEO Forum - May 2026**



# Problem

Inference is the future of AI – but existing hardware is holding back adoption

## Inference is the future of AI

...in edge AI: driving latency, efficiency, privacy and security benefits



...and in high performance computing



... but existing chips fall short



### Low performance

Insufficient to handle the latest AI models



### Power hungry

Energy inefficient or thermally constrained



### Expensive

Customers priced out of mass deployment



### Supply Chain Issue

Existing Chip Supply risk ITAR-Listing



The need? **GPU-level performance at a fraction of the price, resilient supply and low energy consumption**

# Introducing Axelera AI

Datacenter performance, edge efficiency. **Accelerating inference, everywhere.**

## Company

Axelera AI is the **largest AI semiconductor company** in Europe

**2021**

Founding

**250+**

Employees

**60+**

PhDs

**20**

Countries

**\$450M**

Total funding

**10+**

Strategic patents pending

## AI Processing Unit (AIPU)

We build the most **powerful, efficient and affordable AI Processing Unit** for computer vision & GenAI



**METIS**

Computer vision and SLMs at the edge (2024)

**4x**

More power efficient

**3x**

More cost efficient

**2-8x**

Higher performance



**EUROPA**  
Gen AI for high-end edge & HPC (2025/6)



**TITANIA**  
Datacentre (2027/8)

## Technology

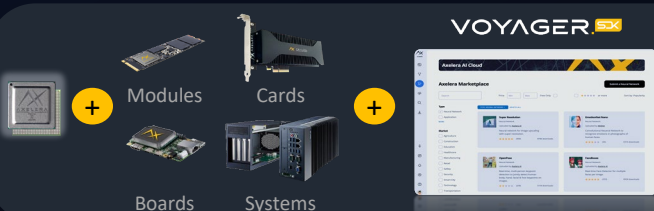
D-IMC for massively efficient AI



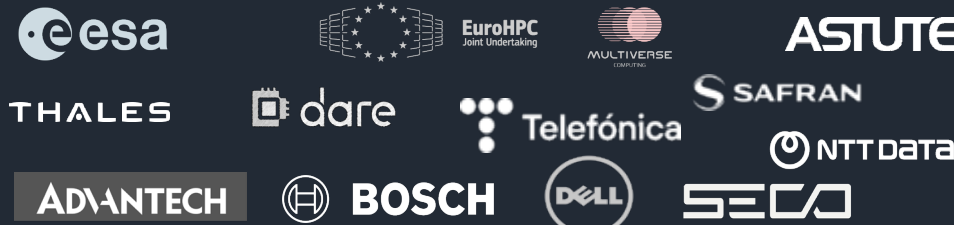
Densely interleaved memory and compute to massively increase efficiency of running AI models

## Product

AIPU, edge-native form factors and leading software stack



## 500+ customers and partners (select examples)



## Investors



## Team (select examples)



# Defense & Aerospace

Low-power edge inference. Defense-grade tough. European sovereign. Future-proof

## Value propositions

### Performance and efficiency

Higher FPS than Hailo at a fraction of NVIDIA's power. Real-time inference at 5W.

### Ruggedized form factor

Standard form factors built to withstand the harshest environments

### European sovereignty

Only production-ready edge AIPU designed entirely in Europe. RISC-V. No ITAR risk

### One platform, future-proof

M.2 today, Europa tomorrow. One SDK, 10-year availability, industry-leading FPS

## Comparison

### Alternative

**NVIDIA**  
(Jetson)

**FPGA**  
(Xilinx, Intel)

**Edge AI chips**  
(Hailo)

### Their approach

15-60W power draw, 2-3yr lifecycle, US export controlled

Custom firmware, 6-12mo dev cycles, high NRE

Lower FPS, no VLM support, fewer models, non-European

### Axelera advantage

**5W typical, 10yr availability, European sovereign**

**Programmable AIPU, SDK-based, weeks not months**

**Higher FPS at lower power, VLM ready, European designed**

## Who we've supported

**THALES** 

+ many others



